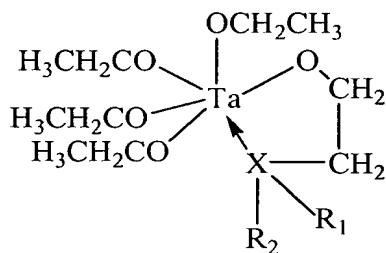


**In the Claims:**

1. (Currently Amended) A semiconductor capacitor comprising:
  - a first electrode;
  - a second electrode; and

a tantalum oxide layer positioned between said first electrode and said second electrode, said tantalum oxide layer formed by depositing at least one precursor and ozone gas, the at least one precursor represented by the formula:



wherein X is selected from the group consisting of nitrogen, sulfur, oxygen, and a carbonyl group; and wherein

R<sub>1</sub> and R<sub>2</sub> are independently alkyl.

2. (Original) The semiconductor capacitor according to Claim 1, wherein R<sub>1</sub> and R<sub>2</sub> are independently selected from C<sub>1</sub> to C<sub>4</sub> alkyl.

3. (Original) The semiconductor capacitor according to Claim 1, wherein R<sub>1</sub> and R<sub>2</sub> are each methyl and X is nitrogen.

4. (Original) The semiconductor capacitor according to Claim 1, wherein the first electrode comprises at least one material selected from the group consisting of polysilicon, a noble metal, and a conductive metal nitride.

5. (Original) The semiconductor capacitor according to Claim 4, wherein the noble metal is selected from the group consisting of Ru, Ir, Pt, and combinations thereof.

6. (Original) The semiconductor capacitor according to Claim 4, wherein the conductive metal nitride is selected from the group consisting of TiN, TaN, WN, and combinations thereof.

7. (Original) The semiconductor capacitor according to Claim 1, wherein the second electrode comprises at least one material selected from the group consisting of polysilicon, a noble metal, and a conductive metal nitride.

8. (Original) The semiconductor capacitor according to Claim 7, wherein the noble metal is selected from the group consisting of Ru, Ir, Pt, and combinations thereof.

9. (Original) The semiconductor capacitor according to Claim 7, wherein the conductive metal nitride is selected from the group consisting of TiN, TaN, WN, and combinations thereof.

10. (Original) The semiconductor capacitor of Claim 1, wherein the tantalum oxide layer is deposited at a temperature ranging of from about 100°C to about 600°C.

11-21. (Canceled).

22. (New) The semiconductor capacitor of Claim 1, wherein the tantalum oxide layer is uniformly deposited.

In re: Jeong-hee Chung et al.

Serial No.: 10/047,706

Filed: January 15, 2002

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**In the Drawings:**

Please replace Figures 1, 2A, 2B and 3 with the attached replacement sheets.

Changes have been made to Fig. 2A on the sheet that includes Fig. 1. Changes have been made to Fig. 2B on the sheet that includes Fig. 3.